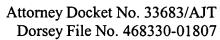
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TRANSMITTAL
FORM
(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

	Application No.	10/817,381	
	Filing Date	April 1, 2004	
	First Named Inventor	Yongli Huang	
	Examiner Name	To Be Asssigned	
	Group Art Unit	3736	
	Attorney Docket No.	33683/AJT (468330-01807)	

		ENCLOSU	RES (chec	k all tha	it apply)		
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Fee Attac	ched	Drawing(s)			Appeal Communication to Board of Appeals and Interferences		
Amendm	ent / Reply	Licensing-relate	ed Papers		Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)		
After Fina	al	Petition			Proprietary Information		
Affidavits	declaration(s)	Petition to Con Provisional App			Status Letter		
Extension	n of Time Request	Power of Attorr Revocation Ch Correspondence	ange of	$\boxtimes$	Other Enclosure(s) (please identify below):		
Express A	Abandonment Request	Terminal Discla	imer		Form PTO/SB/08A-B (2 pgs.); fourteen (14) references; and self-addressed stamped Postcard.		
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	SIGNATURE	OF APPLICANT, A	TTORNEY, O	R AGE	NT		
Firm or Individual name	Jamie J. Zheng, Reg. Aldo J. Test, Reg. No. DORSEY & WHITNE' 4 Embarcadero Cente San Francisco, CA 94 Telephone: 650-494-Facsimile: 650-494-	18,048 / LLP r, Suite 3400 111 3700	Custo	omer N	Number 32940		
Signature	72	uf_					
Date	January 12, 2005		, <u>, , , , , , , , , , , , , , , , , , </u>				
CERTIFICATE OF MAILING							
I hereby certify that this class mail in an envelo	s correspondence is being de pe addressed to Commission	posited with the United S ner for Patents, P.O. Box	tates Postal Sen 1450, Alexandria	vice with	Sufficient postage as first 13-1450 on this date:		
Typed or printed nar	ne Lori Cox	$\bigcap$					
Signature		Toni lè	X	Date	January 12, 2005		





## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

HUANG et al.

U.S. Serial No.: 10/817,381

Filing Date: April 1, 2004

For: Capacitive Ultrasonic Transducers

with Isolation Posts

Examiner:

To Be Assigned

Art Unit:

3736

Confirmation No.: 2603

CERTIFICATE OF MAILING

I hereby certify that this correspondence, including listed enclosures, is being deposited with sufficient postage with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents P.O. BOX 1450, Alexandria, VA 22313-4450 on danyary 12, 2005.

Signed:

Lori Cox

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

In satisfaction of the duty of disclosure under 37 C.F.R. § 1.56, and in accordance with the provisions of 37 C.F.R. §§ 1.97 and 1.98, Applicants wish to draw the attention of the U.S. Patent and Trademark Office to the references cited on the accompanying form PTO/SB/08A-B. In accordance with 1273 Off. Gaz. Pat. Off. 1, 8/5/2003, no copies of U.S. patents and U.S. published applications are enclosed. Copies of all other references are enclosed.

None of the foregoing references are believed to disclose the invention as claimed.

Nothing herein shall constitute an admission concerning the contents of any of the cited references, nor shall the inclusion of a reference herein be considered an admission that the reference constitutes prior art against the invention claimed in the above-identified application. Submission of the present document shall not be construed as an admission that a search has been made or that better art does not exist.

U.S. Serial No.:

10/817,381

Filing Date:

April 1, 2004

As far as is known to the undersigned, this Information Disclosure Statement is being filed within three months of the filing date of a national application, within three months of the date of entry of the national state in an international application, or before the mailing date of a first Office Action on the merits as set forth in 37 C.F.R. § 1.97(b), and therefore no fee is required.

While no fee is believed to be due, if this belief is in error the Commissioner is authorized to charge any additional fees, including extension fees or other relief which may be required, or credit any overpayment to Deposit Account No. 50-2319 (Our Order No. 468330-01807 [33683/AJT]).

Please direct any calls in connection with this application to the undersigned at (650) 494-8700.

Respectfully submitted,
DORSEY & WHITNEY LLP

Date: January 12, 2005

BY:

Dorsey & Whitney LLP Intellectual Property Department Four Embarcadero Center, Suite 3400 San Francisco, CA 94111-4187

Telephone:

(650) 494-8700

Facsimile:

(650) 494-8771

Jamie J. Zheng, Reg. No. 57,167 For Aldo J. Test, Reg. No. 18,048

Customer Number: 32940

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	U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.	U.S. Patent Document Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figure Appear		
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	FOREIGN PATENT DOCUMENTS						
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation it not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at <a href="https://www.uspto.gov">www.uspto.gov</a> or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English Language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the complete application form to the USPTO. Time will vary depending on the individual case. Any comments on the amount of time you require to complete this form and/or suppressions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO:** Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

	Substitute for fo		VPTO	Complete if Known		
	(Modi	ified)		Application Number	10/817,381	
IN	<b>IFORMATION</b>	DISC	CLOSURE	Filing Date	April 1, 2004	
STATEMENT BY APPLICANT				First Named Inventor	Yongli HUANG	
				Art Unit	3736	
(use as many sheets as necessary)				Examiner Name	To Be Assigned	
Sheet	2	of	2	Attorney Docket Number	33683/AJT (468330-01807)	

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Τ°
	C1	AMJADI, H., et al., "Charge Storage In Double Layers Of Silicon Dioxide And Silicon Nitride", 9 <sup>th</sup> International Symposium on Electrets, September 25-30, 1996, pp. 22-27.	
	C2	ANTONIU, A., et al., "Effects of Electrostatic Charge Accumulation During MEMS Fabrication", MEMS, NANO and Smart Systems, 2003. Proceedings. International Conference on 20-23 July 2003, pp. 69-75.	
	СЗ	DENATALE, J., et al., "RF MEMS Reliability", The 12 <sup>th</sup> International Conference on Solid State Sensors, Actuators and Microsystems, Boston, June 8-12, 2003, pp. 943-946.	
,	C4	GILBERT, J.R., et al., "3D Modeling of Contact Problems and Hysteresis in Coupled Electro-Mechanics", IEEE Annual International Conference on Microelectromechanical Systems, 1996, pp. 127-132.	
	C5	GOLDSMITH, C. et al., "Lifetime Characterization Of Capacitive RF MEMS Switches", Microwave Symposium Digest, 2001 IEEE MTT-S International, May 20-25, 2001, 1: 227-230.	
	C6	HUANG, Y., et al., "Capacitive Micromachined Ultrasonic Transducers (cMUTS) With Isolation Posts", Presented at the 2004 IEEE International Ultrasonics Symposium, Montreal, Canada, August 23-27, 2004.	
	C7	HUANG, Y., et al., "Fabricating Capacitive Micromachined Ultrasonic Transducers With Wafer-Bonding Technology", Journal of Microelectromechanical Systems, April 2003, 12(2): 128-137.	
	C8	HUANG, Y., et. al., "A Solution to the Charging Problems in Capacitive Micromachined Ultrasonic Transducers (CMUTS)", accepted for publication, IEEE Trans., <i>Ultrason., Ferroelect. Freg. Contr.</i>	
	C9	JIN, X., et al., "Fabrication and Characterization of Surface Micromachined Capacitive Ultrasonic Immersion Transducers", IEE Journal of Microelectromechanical Systems, March 1999, 8(1): 100-114.	
	C10	REID, J.R., et al., "Noncontact Measurement of Charge Induced Voltage Shift in Capacitive MEM-Switches", IEEE Microwave and Wireless Components Letters, September 2003, 13(9): 367-369.	
	C11	SHEA, H.R., et al., "Effects of Electrical Leakage Currents on MEMS Reliability and Performance", IEEE Transactions on Device and Materials Reliability, June 2004, 4(2): 198-207.	1
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	C13	YUAN, X., et al., "Initial Observation and Analysis of Dielectric-Charging Effects on RF MEMS Capacitive Switches", Microwave Symposium Digest, 2004 IEEE MTT-S International, June 6-11, 2004, 3(3): 1943-1946.	
	C14	YUANG, Y., et al., "New Fabrication Process for Capacitive Micromachined Ultrasonic Transducers": 1-4.	
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